MPC567XADAT516

Errata & Change History

Rev A   May 2009

Errata   Initial release
- SCH crystal value incorrect, correct 40MHz crystal installed during assembly.
- Wire modification applied to correct missing GND connection at MICTOR connector, J4.
- Several option headers not populated due to interference with socket body. Default option selections at these headers applied using cut-traces and jumper wire.
- Reference designators are R23, R25 are reversed.

Rev B   Feb 2010

Not built

Rev C   Jun 2010

Change   Socket initially removed from design but added back later.
- Schematic is updated to correct memory addressing and allow burst mode access.
- Cut-Traces and jumper wire modifications removed and all option headers installed.
- Updated crystal value on SCH to correct value.
- Silkscreen at swap R23, R25 corrected.

Errata   None

NOTE: For Rev C boards built with MCU mounted on interposer, all option headers are populated with shunts placed for default option selection. For Rev C boards built with sockets, several option headers are not populated due to interference with the socket body. Jumper wire modifications apply default option selection for these boards.

Rev D   Nov 2010

Change   Socket is added to board.
- Interfering option headers not populated due to interference with socket body.
- Updated SCH to add default option selections using cut- traces.
- Board name changed to MPC567XADAT516 to reflect support for both MPC5674F (Mamba) and MPC6756R (Cobra) MCU's

Web Site: www.axman.com

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Errata
- CANTX_B and CANRX_B signals transposed at MCU. This issue may be corrected by applying a wire modification on MPC567XEVBMB at JP2, CAN_SEL option header.
- REGCTL & REGSEL option headers improperly configured. For proper usage, cut trace between JP8, pin 1 and pin 2. Weak, internal pull-down on REGSEL selects LDO operation if input is left floating. Alternately, the user may apply a jumper wire between JP8, pin 2 and pin 3.

Rev E   June 2011

Change   Correct CANTX_B and CANRX_B routing to MCU.

Errata
- REGCTL & REGSEL option headers improperly configured. For proper usage, cut trace between JP8, pin 1 and pin 2. Internal pull-up on REGCTL selects LDO operation.