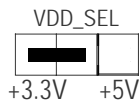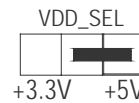# EVB51JM128 ERRATA

The EVB51JM128 is an evaluation board designed to allow the user to quickly and easily evaluate the MCF51JM128 microcontroller. The MCU applies an integrated OTG capable USB physical layer (PHY) interface. Supporting circuitry on-board provides functionality for OTG-, Host-, and Device-mode operation.

To ensure proper operation of the USB-Lite stack by CMX, the VDD_SEL jumper must be moved to the +5V setting (1 & 2). Please refer to the Default Jumper Settings as shown in the EVB51JM128 Labs Document (EVB51JM128LABS) for correct placement of the VDD_SEL jumper.

Factory configuration

Shunt position to enable CMX demo

Figure 1: Shunt Placement Change